

Electronic Patent Application Fee Transmittal

Application Number:	10811835
Filing Date:	30-Mar-2004
Title of Invention:	Method of designing a circuit layout of a semiconductor device
First Named Inventor/Applicant Name:	Kazuaki Goto
Filer:	Donald R. Studebaker/Pamela Patrick
Attorney Docket Number:	030712-29

Filed as Large Entity

Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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Basic Filing:

Pages:

Claims:

Miscellaneous-Filing:

Petition:

Patent-Appeals-and-Interference:

Notice of appeal	1401	1	540	540
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Post-Allowance-and-Post-Issuance:

Extension-of-Time:				
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Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension - 3 months with \$0 paid	1253	1	1110	1110
Miscellaneous:				
Total in USD (\$)				1650